

# ALTOII MEMORY LAYOUT

BIT	0-17777		20000-37777		40000-57777		60000-77777		100000-117777		120000-137777		140000-157777		160000-176777	
	EVEN	ODD	EVEN	ODD	EVEN	ODD	EVEN	ODD	EVEN	ODD	EVEN	ODD	EVEN	ODD	EVEN	ODD
0	1-16	1-18	1-26	1-28	1-36	1-38	1-46	1-48	1-56	1-58	1-66	1-68	1-76	1-78	1-86	1-88
1	2-16	2-18	2-26	2-28	2-36	2-38	2-46	2-48	2-56	2-58	2-66	2-68	2-76	2-78	2-86	2-88
2	3-16	3-18	3-26	3-28	3-36	3-38	3-46	3-48	3-56	3-58	3-66	3-68	3-76	3-78	3-86	3-88
3	4-16	4-18	4-26	4-28	4-36	4-38	4-46	4-48	4-56	4-58	4-66	4-68	4-76	4-78	4-86	4-88
4	1-11	1-13	1-21	1-23	1-31	1-33	1-41	1-43	1-51	1-53	1-61	1-63	1-71	1-73	1-81	1-83
5	2-11	2-13	2-21	2-23	2-31	2-33	2-41	2-43	2-51	2-53	2-61	2-63	2-71	2-73	2-81	2-83
6	3-11	3-13	3-21	3-23	3-31	3-33	3-41	3-43	3-51	3-53	3-61	3-63	3-71	3-73	3-81	3-83
7	4-11	4-13	4-21	4-23	4-31	4-33	4-41	4-43	4-51	4-53	4-61	4-63	4-71	4-73	4-81	4-83
8	1-17	1-19	1-27	1-29	1-37	1-39	1-47	1-49	1-57	1-59	1-67	1-69	1-77	1-79	1-87	1-89
9	2-17	2-19	2-27	2-29	2-37	2-39	2-47	2-49	2-57	2-59	2-67	2-69	2-77	2-79	2-87	2-89
10	3-17	3-19	3-27	3-29	3-37	3-39	3-47	3-49	3-57	3-59	3-67	3-69	3-77	3-79	3-87	3-89
11	4-17	4-19	4-27	4-29	4-37	4-39	4-47	4-49	4-57	4-59	4-67	4-69	4-77	4-79	4-87	4-89
12	1-12	1-14	1-22	1-24	1-32	1-34	1-42	1-44	1-52	1-54	1-62	1-64	1-72	1-74	1-82	1-84
13	2-12	2-14	2-22	2-24	2-32	2-34	2-42	2-44	2-52	2-54	2-62	2-64	2-72	2-74	2-82	2-84
14	3-12	3-14	3-22	3-24	3-32	3-34	3-42	3-44	3-52	3-54	3-62	3-64	3-72	3-74	3-82	3-84
15	4-12	4-14	4-22	4-24	4-32	4-34	4-42	4-44	4-52	4-54	4-62	4-64	4-72	4-74	4-82	4-84
HO	1-20		1-30		1-40		1-50		1-60		1-70		1-80		1-90	
H1	2-20		2-30		2-40		2-50		2-60		2-70		2-80		2-90	
H2	3-20		3-30		3-40		3-50		3-60		3-70		3-80		3-90	
H3	4-20		4-30		4-40		4-50		4-60		4-70		4-80		4-90	
H4	1-15		1-25		1-35		1-45		1-55		1-65		1-75		1-85	
H5	2-15		2-25		2-35		2-45		2-55		2-65		2-75		2-85	
P	3-15		3-25		3-35		3-45		3-55		3-65		3-75		3-85	

NOTE: 1. Card-Chip location.  
 2. The Memory Configuration Switch complements the high-order memory address bit.